

# 1A

**FETs AND BJTs ARE CCDs**

## Active Devices work on the Principle of Charge-Controlled Devices:

The *rate of charge collection* at the output port (the output current) is controlled by the *amount* of charge placed on the input port.

## Active Devices work on the Principle of Charge-Controlled Devices:

The *rate of charge collection* at the output port (the output current) is controlled by the *amount* of charge placed on the input port.

**The RESISTOR** is a reduced version of an active device.

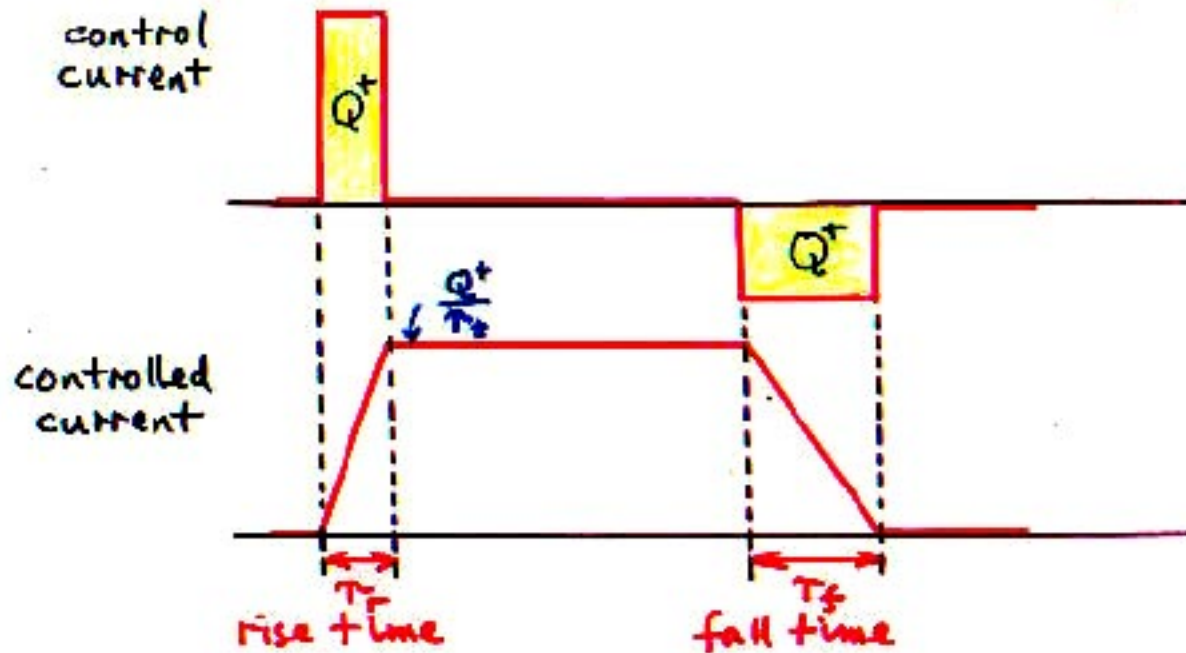
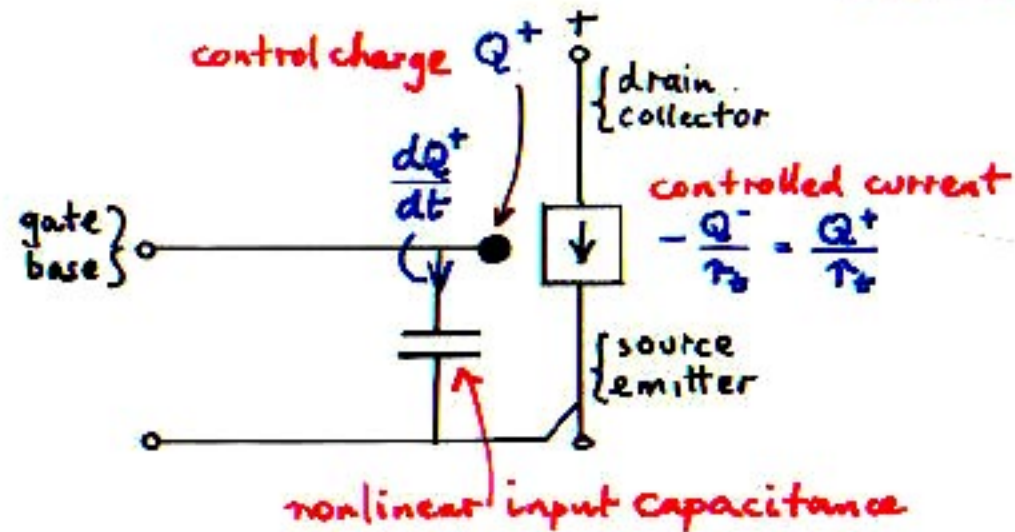
The Resistor is a 1-port device in which charge control and collection are performed by one electrode.

The Resistor is a 1-port device in which charge control and collection are performed by one electrode.

An Active Device is created when the control and collection functions are performed by two separate electrodes, as in

**The FET**, the Field Effect Transistor

MOSFET and BJT (and JFET and TUBE) are CHARGE-CONTROLLED DEVICES



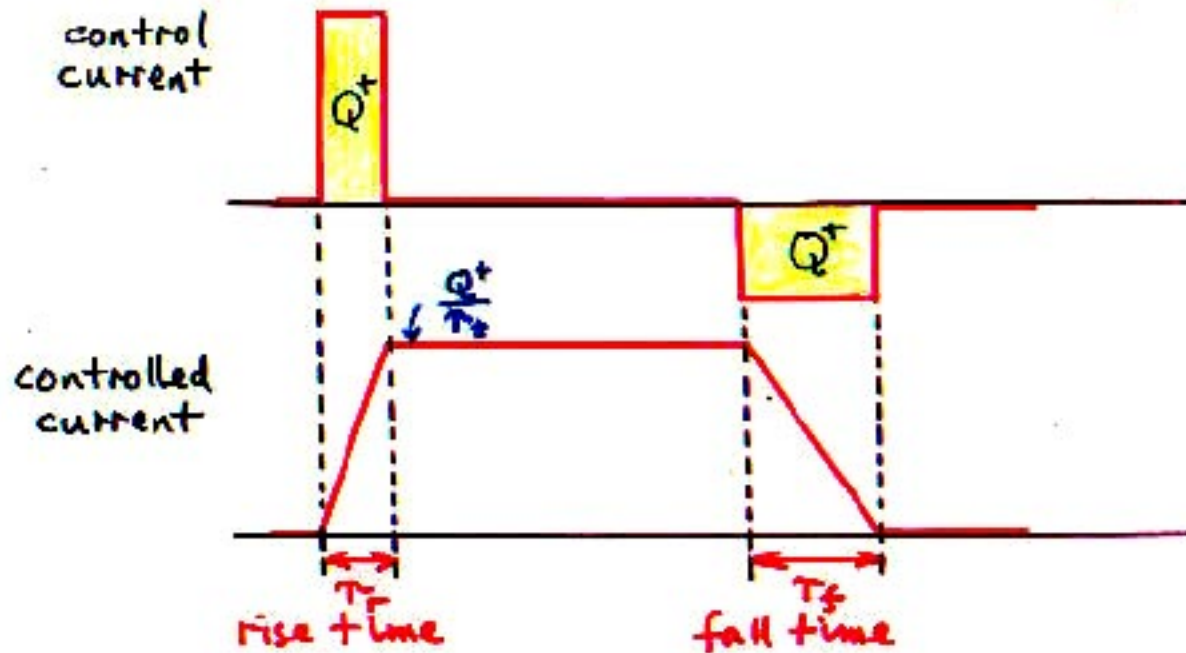
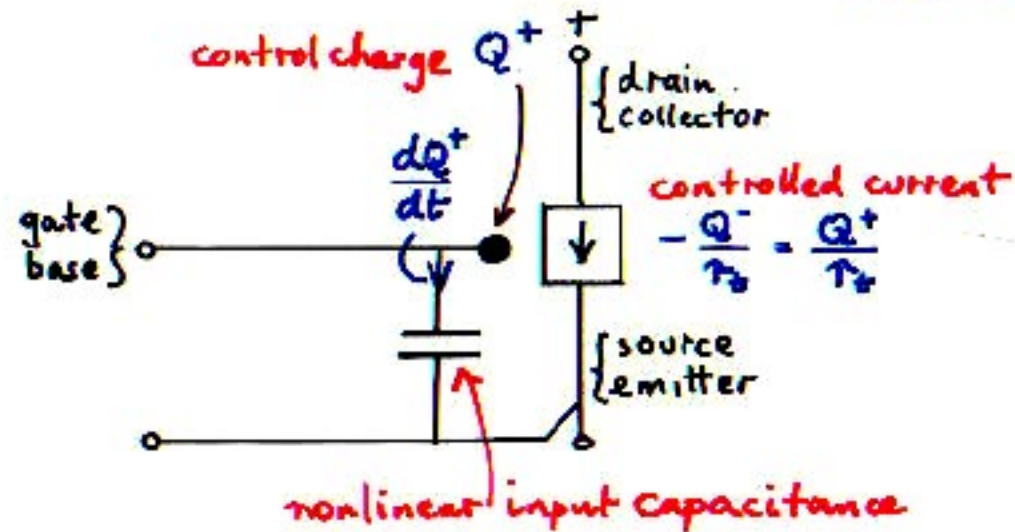
The FET is a Charge-Controlled Device in that the drain, or output, current is controlled by the gate, or input, charge.

The FET is a Charge-Controlled Device in that the drain, or output, current is controlled by the gate, or input, charge.

**The BJT**, the Bipolar Junction Transistor, is also a Charge-Controlled Device, and its basic operation is exactly the same.



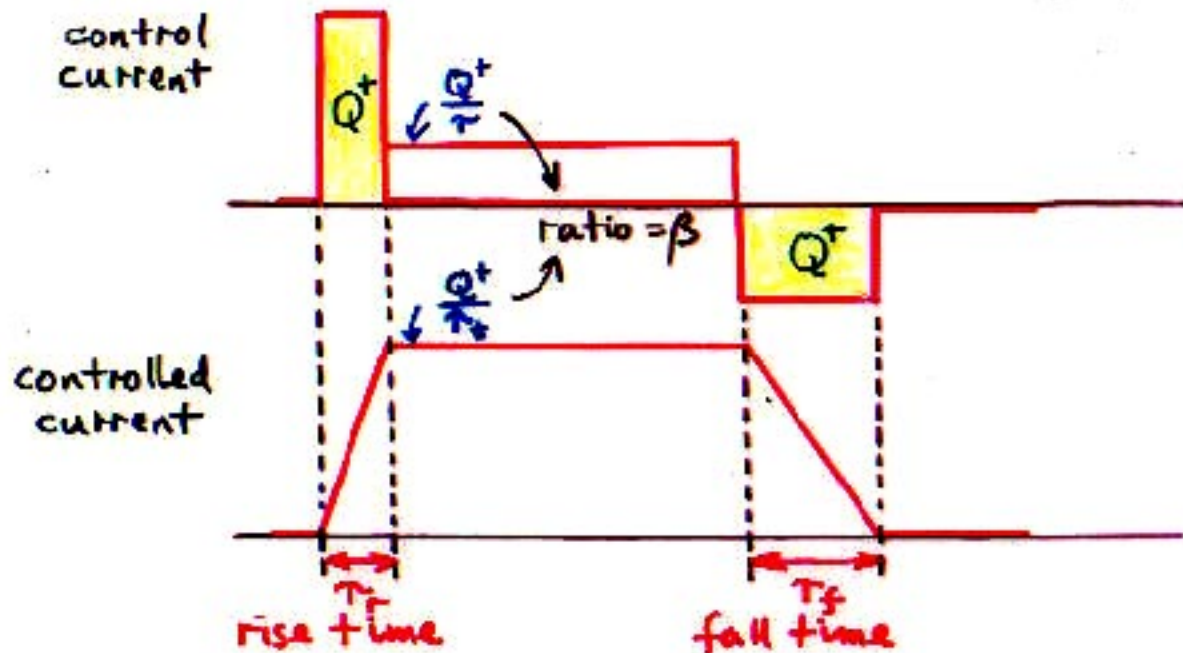
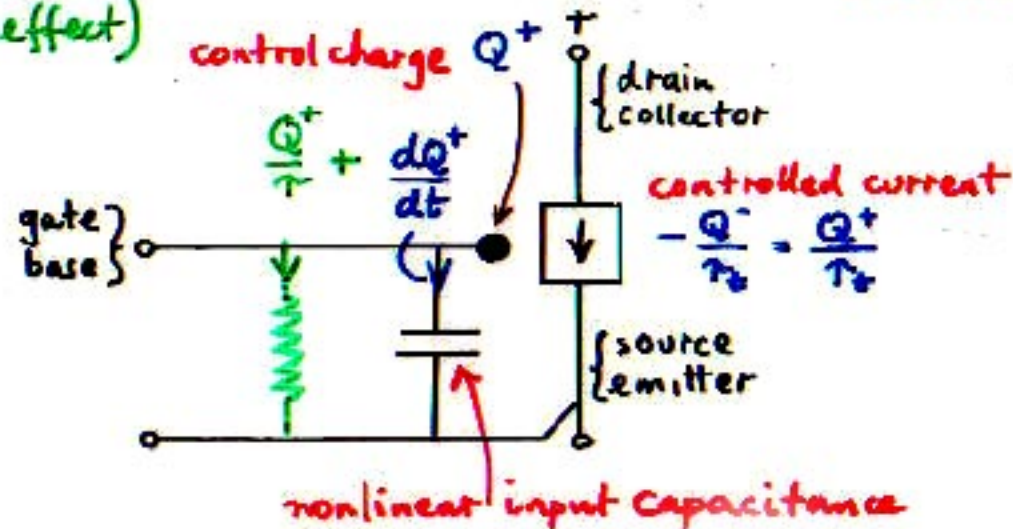
MOSFET and BJT (and JFET and TUBE) are CHARGE-CONTROLLED DEVICES



MOSFET and BJT (and JFET and TUBE) are CHARGE-CONTROLLED

BJT requires a "maintenance" control current (second-order effect)

DEVICES



## Principle of Charge-Controlled Devices:

The *rate of charge collection* at the output port (the output current) is controlled by the *amount* of charge placed on the input port.

These animations illustrate only the basic principle of Active Devices: they are all charge-controlled.

The voltage or current needed at the input to provide a given amount of controlled charge is, from a device point of view, a secondary consideration.

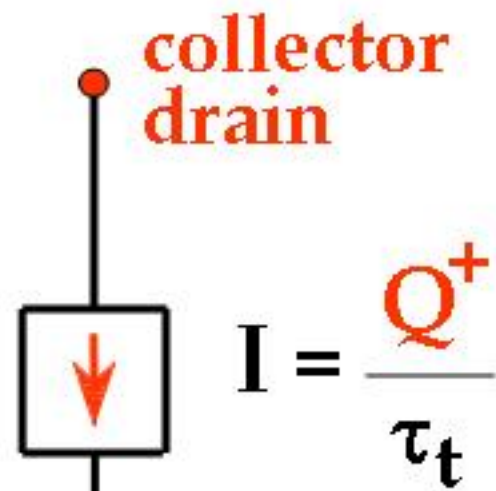
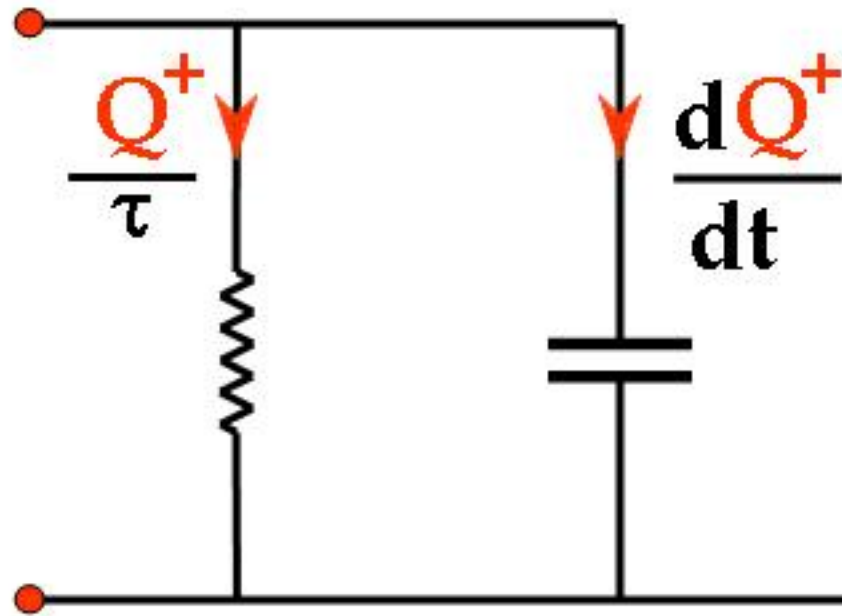
The presence of an input capacitance is inherent in the device.

To make the device operate fast, the controlling charge must be supplied or withdrawn fast, requiring a large transient drive current. This is the same for the FET and for the BJT.

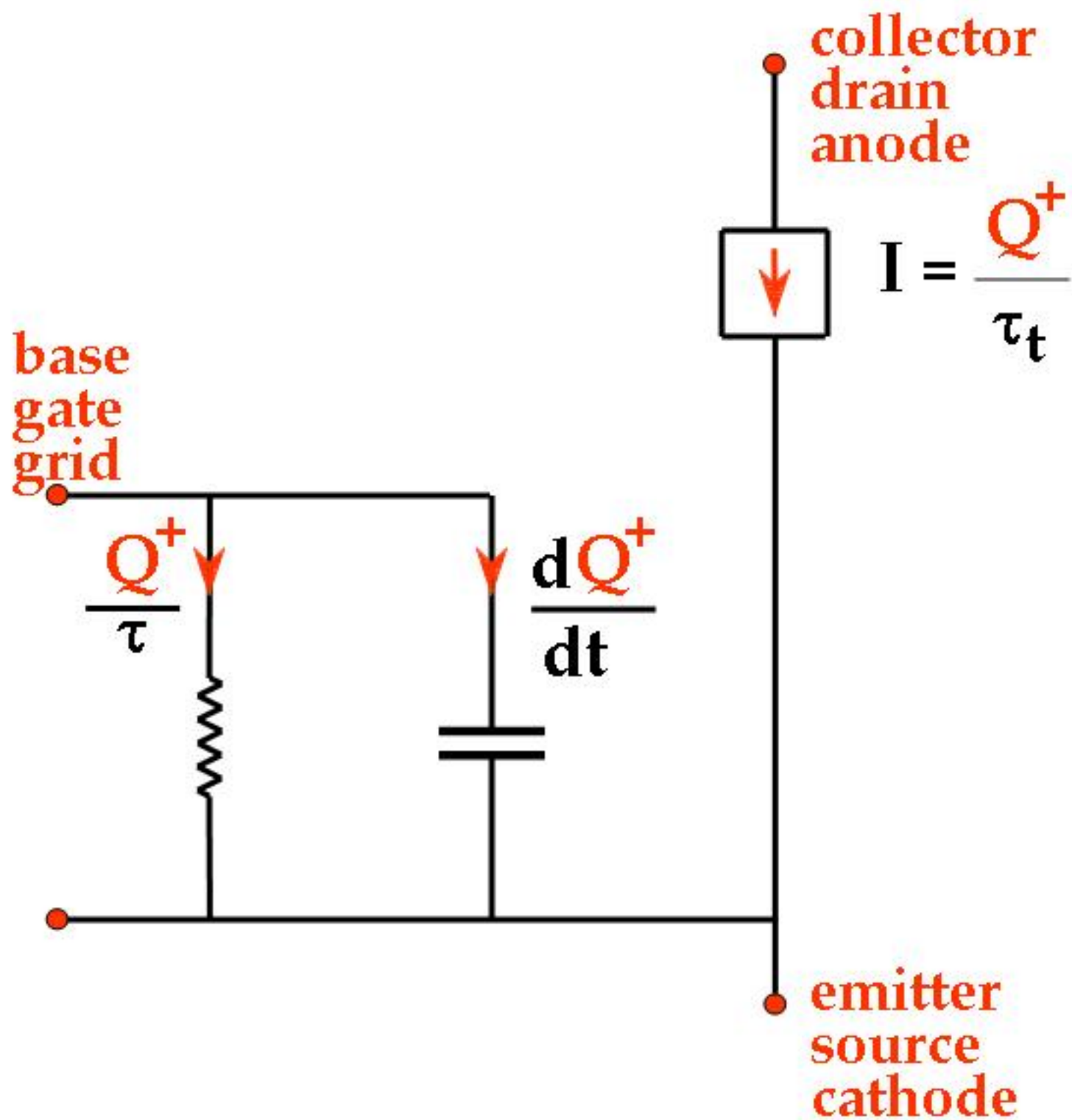
In the BJT, the controlling charge must be maintained, in the presence of recombination, by a relatively small dc current.

# Terminal Terminology

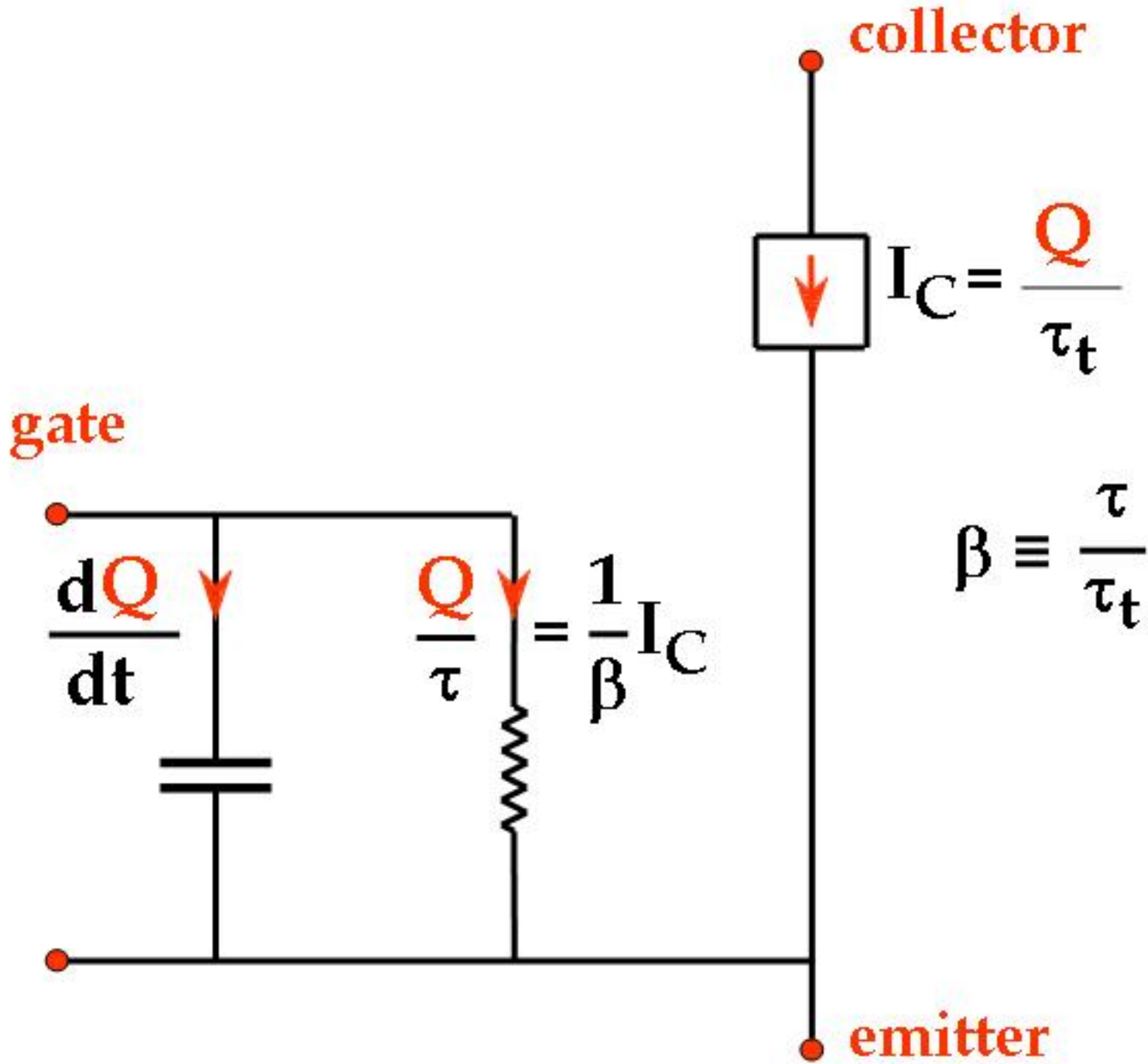
base  
gate



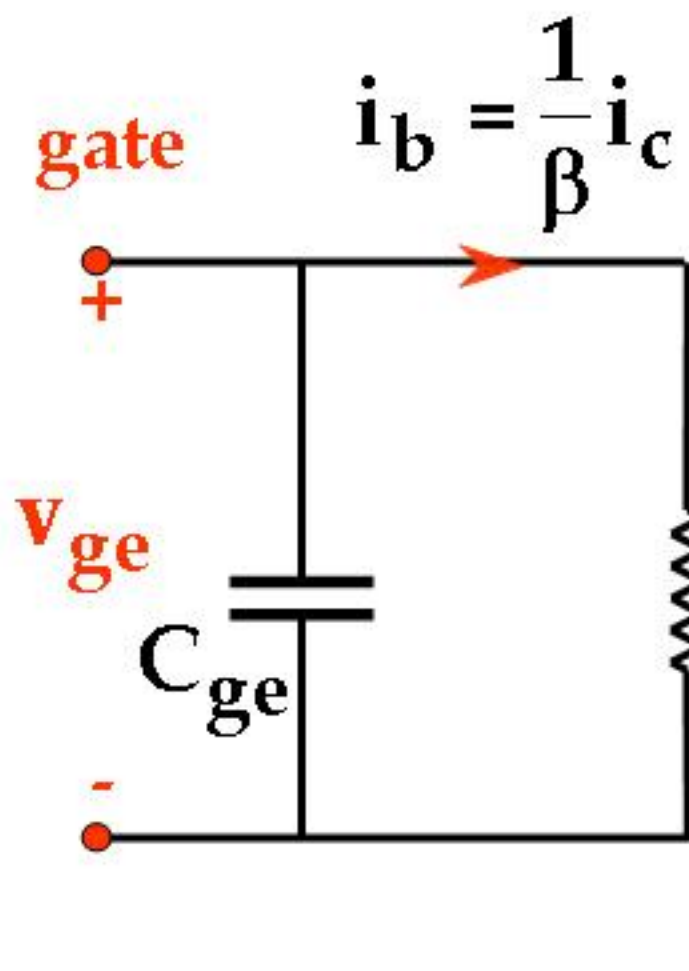
emitter  
source







Model 1: excitation  $v_{ge}$



**collector**



$$i_c = g_m v_{ge}$$

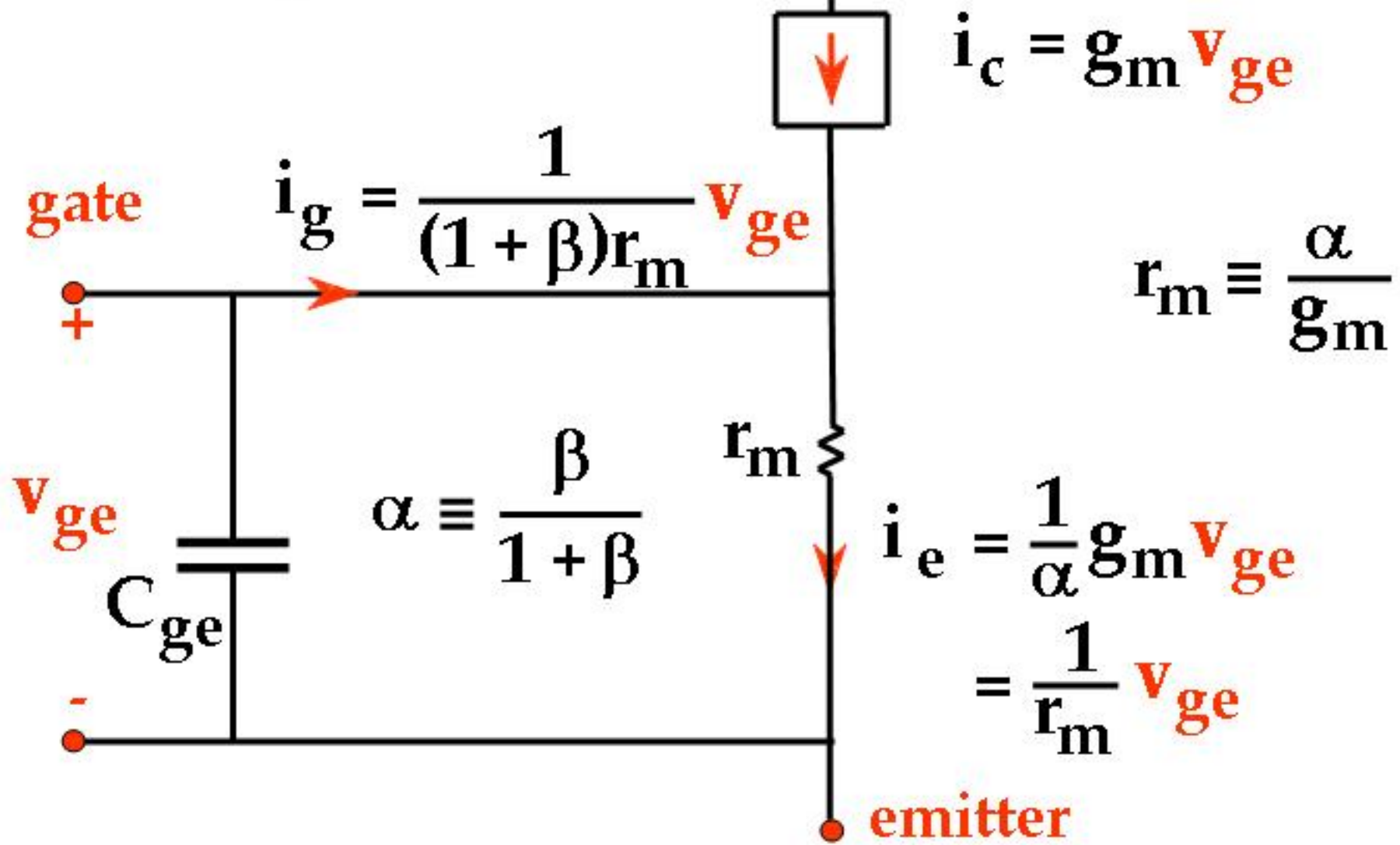
$$g_m \equiv \frac{dI_C}{dV_{GE}}$$

$$C_{ge} \equiv \frac{dQ}{dV_{GE}}$$

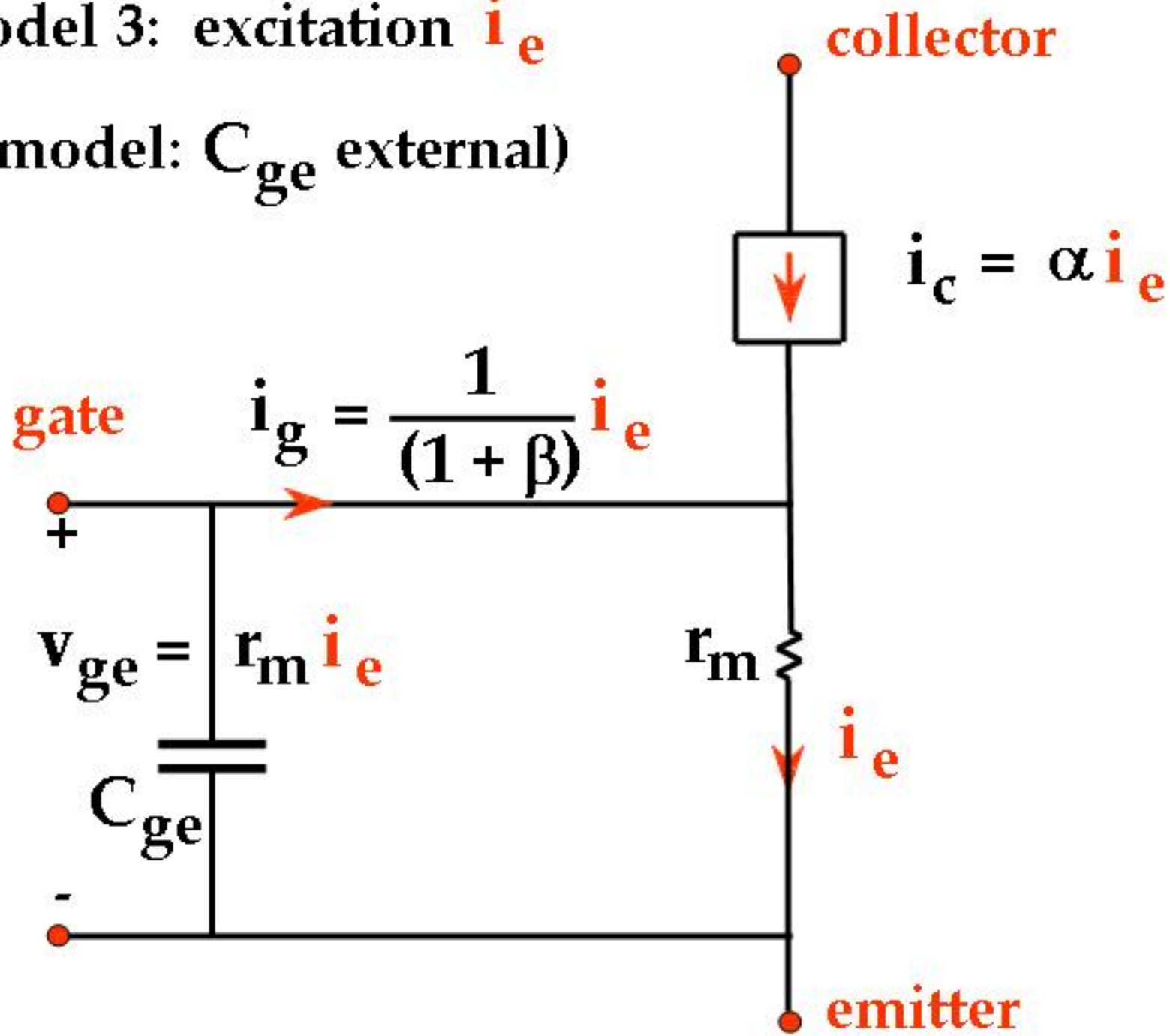
**emitter**

Model 2: excitation  $v_{ge}$

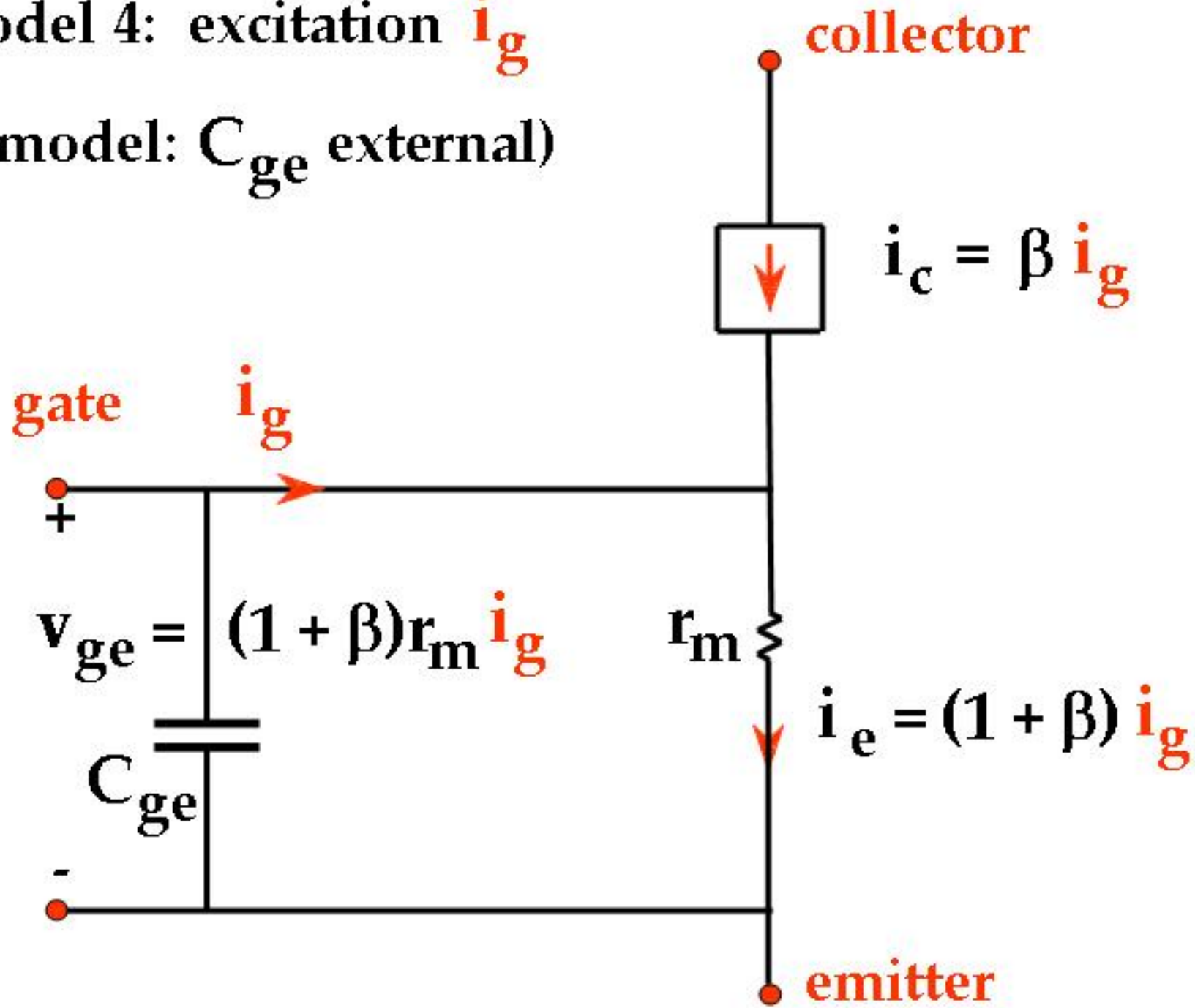
(T model:  $C_{ge}$  external)



Model 3: excitation  $i_e$   
(T model:  $C_{ge}$  external)

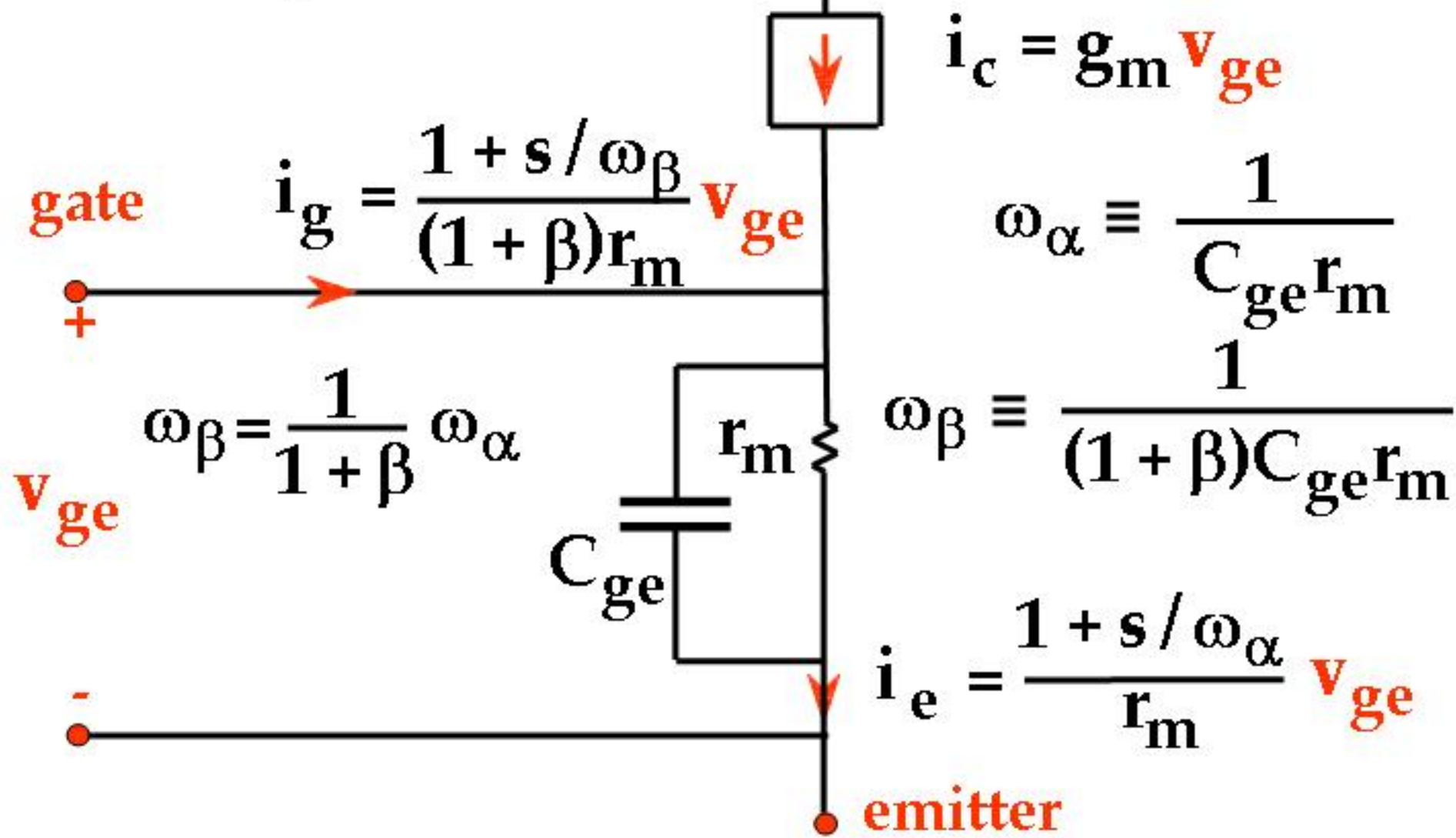


Model 4: excitation  $i_g$   
(T model:  $C_{ge}$  external)



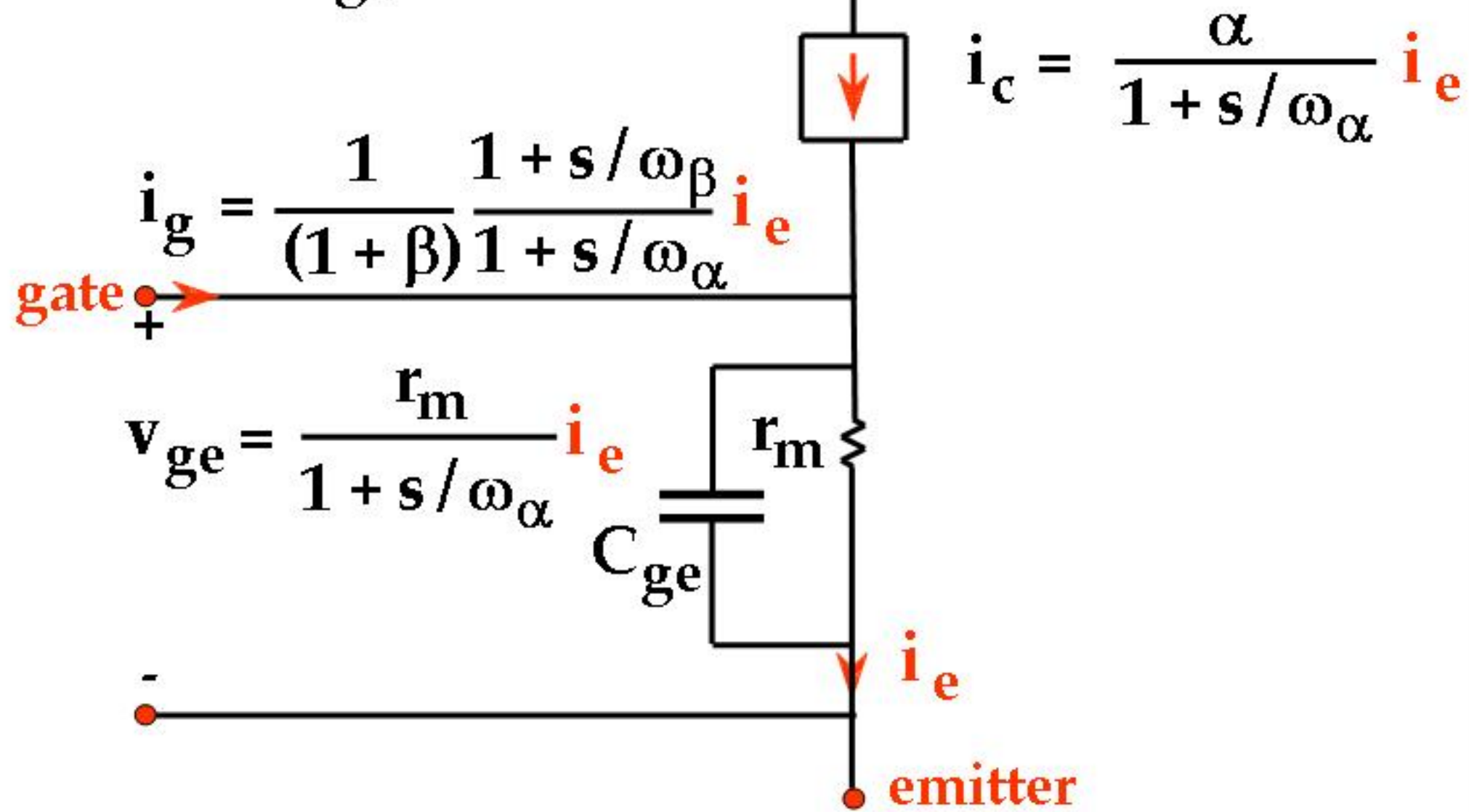
Model 5: excitation  $v_{ge}$

(T model:  $C_{ge}$  internal)

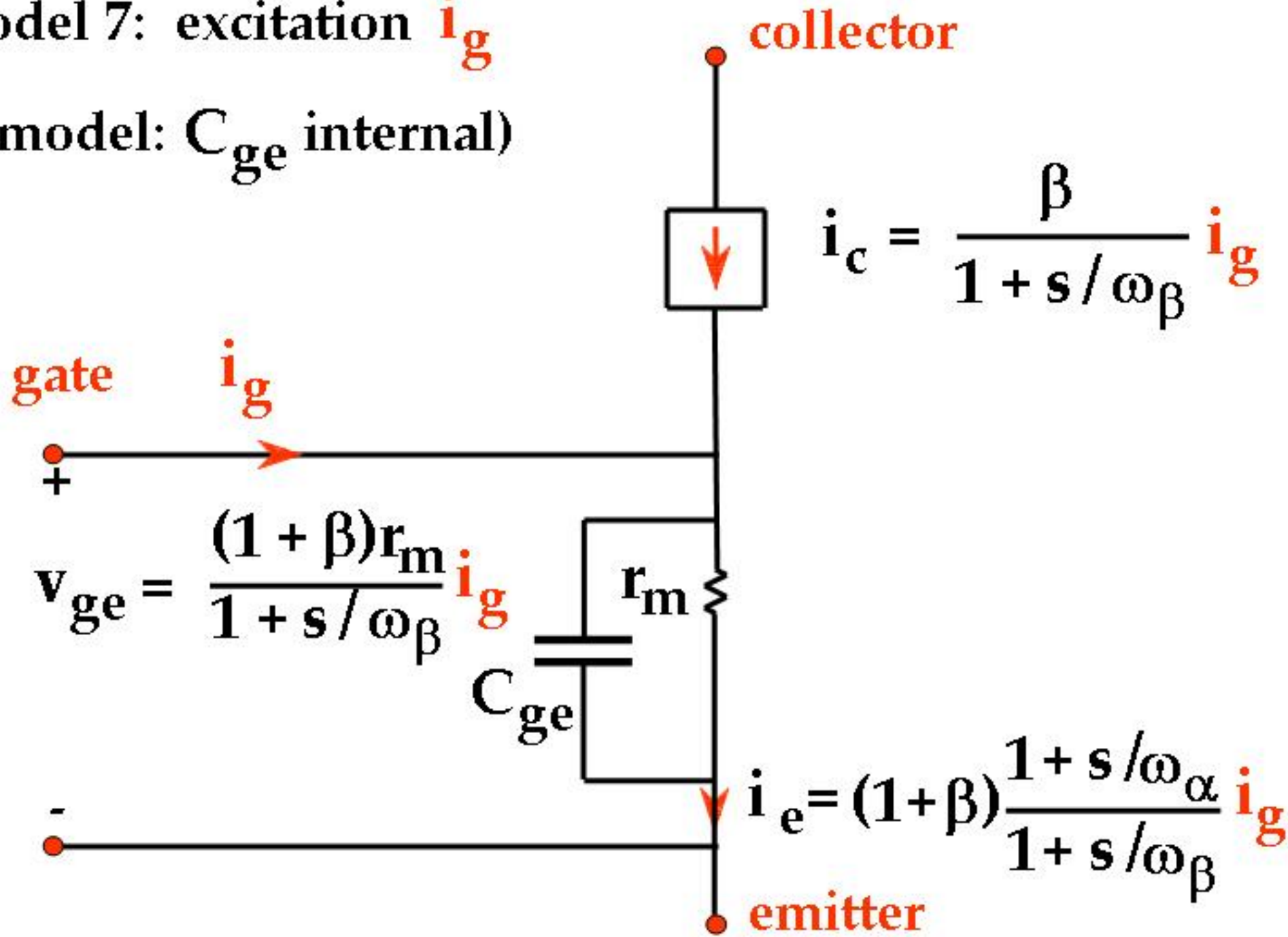


Model 6: excitation  $i_e$

(T model:  $C_{ge}$  internal)



Model 7: excitation  $i_g$   
 (T model:  $C_{ge}$  internal)





**Dependence of transconductance  $g_m \equiv dI_C / dV_{GE}$   
upon nonlinearity, with offsets ignored:**

**TUBE:  $I_C = CV_{GE}^{3/2}$        $g_m = (3/2)CV_{GE}^{1/2}$**

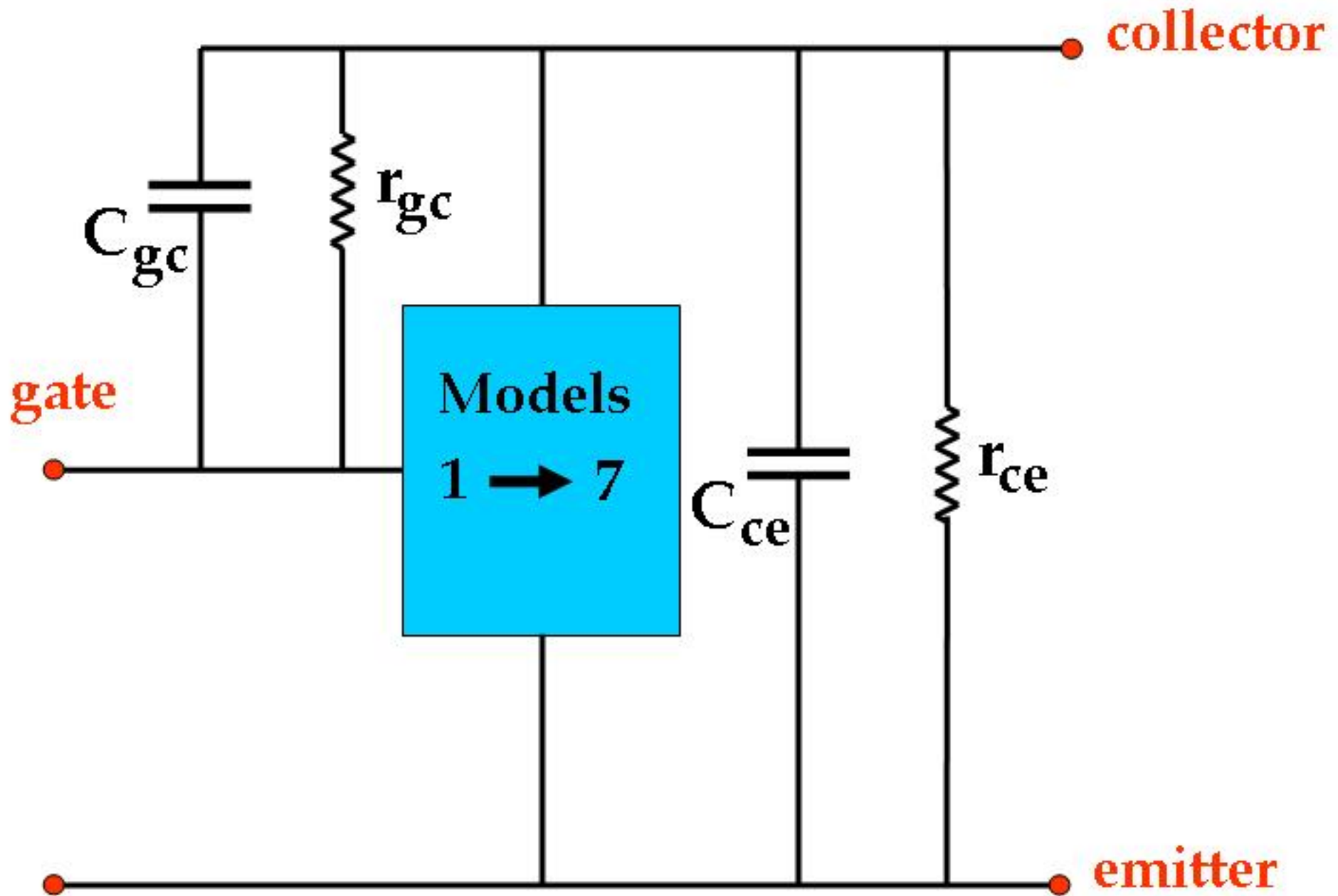
**FET:  $I_C = CV_{GE}^2$        $g_m = 2CV_{GE}$**

**BJT:  $I_C = \alpha C e^{V_{GE}/V_t}$        $g_m = I_C / V_t$   
 $r_m = \alpha / g_m = V_t / I_E$**

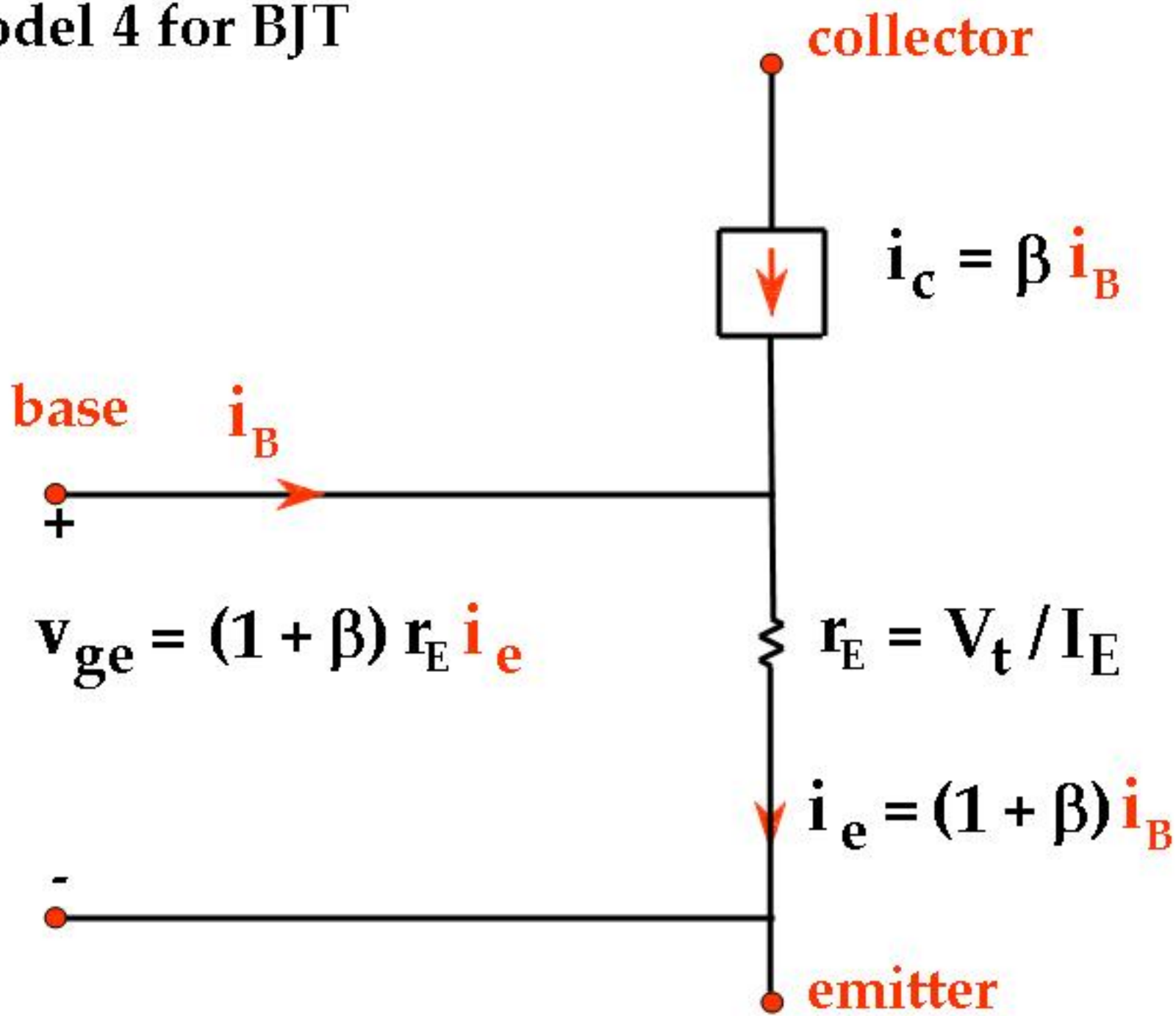
**where  $V_t = kT/e$  is the thermal voltage  
= 25.7mV @ 25° C**

**The constants C are different, and depend upon  
device materials, structure, and geometry**

# Addition of some "parasitic" elements: the Pi Model



# Model 4 for BJT



# Model 3 for FET

